# Techniques for Improving Software Productivity

Instructor: Mooly Sagiv

TA: Kalev Alpernas

http://cs.tau.ac.il/~msagiv/courses/software-productivity.html

Slides from Eran Yahav, Zach Tatlock and the Noun Project, Wikipedia

# Course Prerequisites

- Logic in Computer Science
- Software Project

### Course Requirements

- The students must solve all homework assignments but one (40%)
  - Apply a tool
  - − ~10 hours per project
  - First assignment available on next Thursday
- 60% final exam

# Software is Everywhere











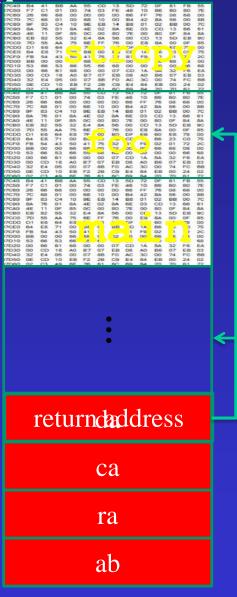


# Explositable is Everywhere

Exploitable Soft re is Everywhere Sony PlayStation RSA hacked, h: An information leaks Stuxnet Worm Still SA's cor Out of Control at Security Advisory for Adobe Flash ed Acrobat Security Robert Read RSA tokens may be Player, Adobe Read behind Player, According to behind major This vulnerability c note. ially security 848 State the Supplication the

Buffer Overrun

```
void foo (char *x) {
   char buf[2];
\longrightarrow strcpy(buf, x);
 int main (int argc, char *argv[]) {
   foo(argv[1]);
 > ./a.out
source code
abracadabra
  Segmentation
  fault
```



termina

memory

# Buffer Overrun Exploits

```
int check_authentication(char *password) {
   int auth_flag = 0;
   char password_buffer[16];
   strcpy(password_buffer, password);
   if(strcmp(password_buffer, "brillig") == 0) auth_flag = 1;
   if(strcmp(password_buffer, "outgrabe") == 0) auth_flag = 1;
   return auth_flag;
int main(int argc, char *argv[]) {
  if(check_authentication(argv[1])) {
    printf("\n-=-=---\n");
    printf(" Access Granted.\n");
    printf("-=-=---\n");
 else
    printf("\nAccess Denied.\n");
```

#### Attack



AAAAAAAAAAA

-\_-\_-

Access Granted. 65

-=-=-=-=-=-=-=-=-=-



- A sailor on the U.S.S. Yorktown entered a 0 into a data field in a kitchen-inventory program
- The 0-input caused an overflow, which crashed all LAN consoles and miniature remote terminal units
- The Yorktown was dead in the water for about two hours and
   45 minutes

#### One Day Last Summer...

#### The New York Times

The Stock Market Bell Rings, Computers Fail, Wall Street Cringes

By NATHANIEL POPPER JULY 8, 2015

Problems with technology have at times roiled global financial markets, but the 223-year-old <u>New York Stock</u> <u>Exchange</u> has held itself up as an oasis of humans ready to step in when the computers go haywire.

On Wednesday, however, those working on the trading floor were left helpless when the computer systems at the exchange went down for nearly four hours in the middle of the day, bringing an icon of capitalism's ceaseless energy to a costly halt.

The exchange ultimately returned to action shortly before the closing bell,



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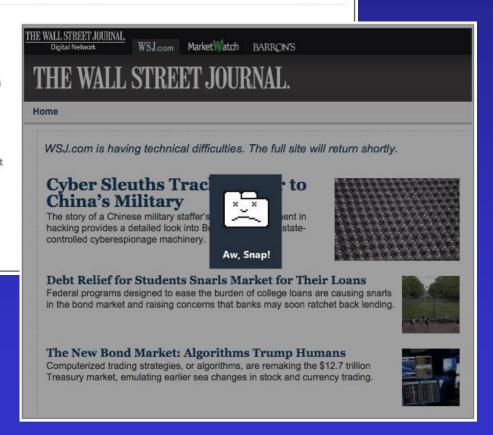
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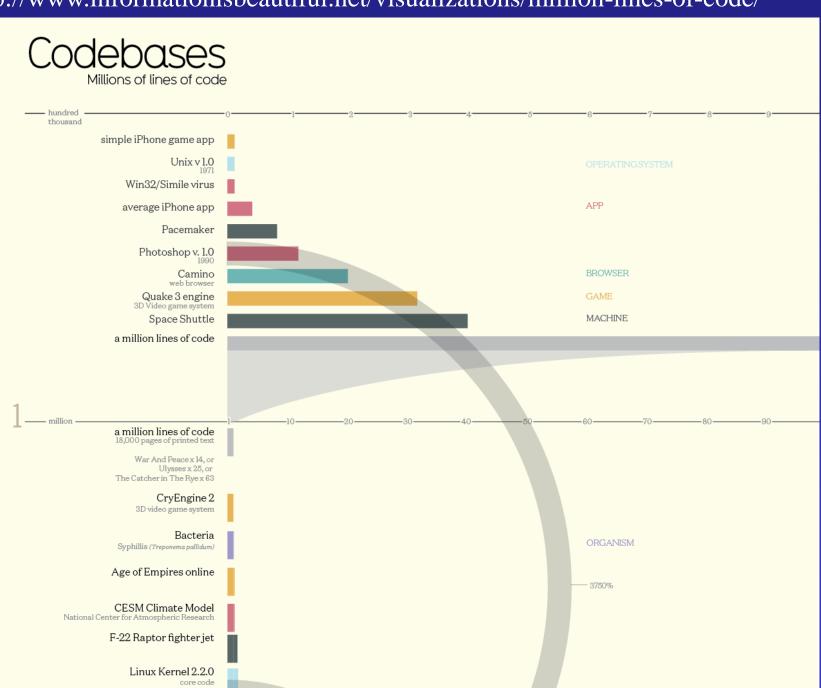
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# Software is Complex

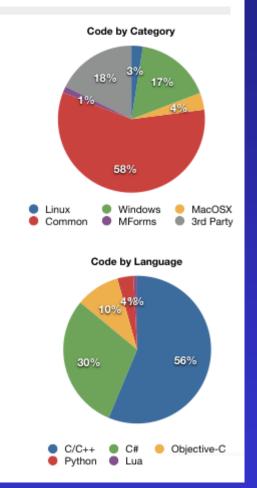
#### http://www.informationisbeautiful.net/visualizations/million-lines-of-code/





#### MySQL Workbench 5.2 Code Statistics

	Files	Lines of Code
Linux	65	43782
Windows	430	93065
MacOSX	97	19198
Common	1497	325001
MForms	36	9499
3rd Party	457	201401
Total	2582	691946
C/C++	752	340492
C#	397	100297
Objective-C	129	29129
Python	47	15260
Lua	11	3061
XML Files	63	
Icons	390	
Glade UI	24	
NIB	89	
.NET Designer	76	



### Cost of software bugs

- 59.5 billion dollars in the US due to software bugs
- Software security
  - Cars, Planes, Radiotherapy, Internet, ....
- Software agility

## Improving Software Productivity

- High level programming languages
  - Abstractions
- Software Engineering
  - Software designs
- Software tools
  - Software testing
  - Software debugging
  - Formal Verification

## Software Testing

• Goal: "to affirm the quality of software systems by systematically exercising the software in carefully controlled circumstances" [E. F. Miller, Introduction to Software Testing Technology]

### The Testing Spectrum

- Unit Testing: basic unit of software
- Integration Testing: combination
- System's testing: end-to-end
- Acceptance testing: client check

## Testing Techniques

- Random testing: Runs the program on random inputs
- Symbolic techniques
- Concolic techniques
- Adequacy of test suit
  - Coverage
  - Mutation testing: Modify the program in a small way
    - Check the adequacy of the test suit

#### Symbolic vs. Concrete Testing

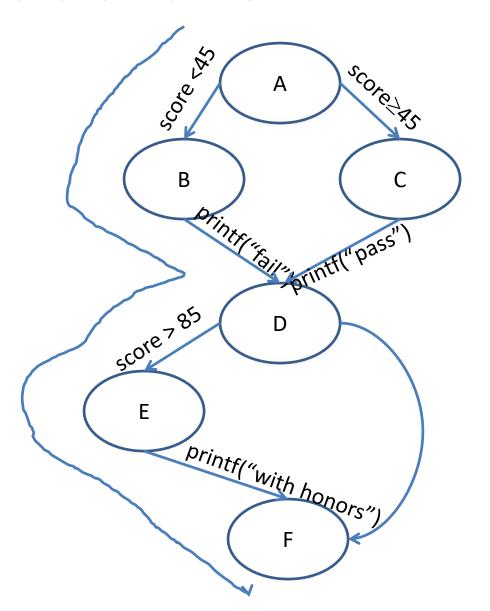
**Mooly Sagiv** 

#### Program Path

- Program Path
  - A path in the control flow of the program
    - Can start and end at any point
    - Appropriate for imperative programs
- Feasible program path
  - There exists an input that leads to the execution of this path
- Infeasible program path
  - No input that leads to the execution

#### Infeasible Paths

```
void grade(int score) {
A: if (score <45) {
B: printf("fail");
    }
    else
C: printf("pass");
    }
D: if (score > 85) {
E: printf("with honors");
    }
F:
}
```



#### Concrete vs. Symbolic Executions

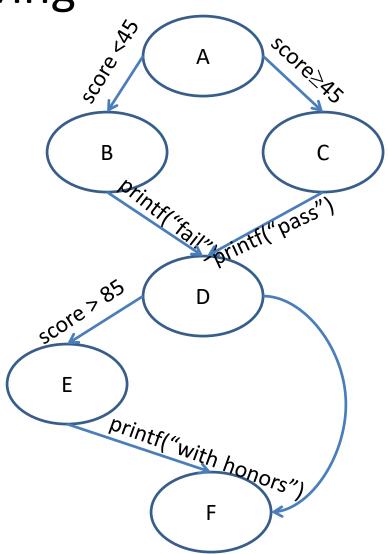
- Real programs have many infeasible paths
  - Ineffective concrete testing
- Symbolic execution aims to find rare errors

#### Symbolic Testing Tools

- EFFIGY [King, IBM 76]
- PEX [MSR]
- SAGE [MSR]
- SATURN[Stanford]
- KLEE[Stanford]
- Java pathfinder[NASA]
- Bitscope [Berkeley]
- Cute [UIUC, Berkeley]
- Calysto [UBC]

# Finding Infeasible Paths Via Constraint Solving

```
void grade(int score) {
    A: if (score <45) {
    B: printf("fail");
      else
        printf("pass");
     D: if (score > 85) {
          printf("with honors");
    F:
score < 45 \land score > 85
                              UNSAT
```



#### Plan

- Random Testing
- Symbolic Testing
- Concolic Testing

#### Fuzzing [Miller 1990]

- Test programs on random unexpected data
- Can be realized using black/white testing
- Can be quite effective
  - Operating Systems
  - Networks
- ...
- Usually implemented via instrumentation
- Tricky to scale for programs with many paths

#### **Success Stories Fuzzing**

- Crashes to Unix [90s]
- Crashes to all systems
  - American Fuzzy Lop http://lcamtuf.coredump.cx/afl/

#### Symbolic Exploration

- Execute a program on symbolic inputs
- Track set of values symbolically
- Update symbolic states when instructions are executed
- Whenever a branch is encountered check if the path is feasible using a theorem prover call

#### Symbolic Execution Tree

- The constructed symbolic execution paths
- Nodes
  - Symbolic Program States
- Edges
  - Potential Transitions
- Constructed during symbolic evaluation
- Each edge requires a theorem prover call

#### Simple Example

```
1)int x, y;
2) if (x > y) {
                                             pc=1, x =s1, y=s2
 3) x = x + y;
 4) y = x - y;
 5) x = x - y;
 6) if (x > y)
                                             pc=2, x =s1, y=s2
  7) assert false;
                                                                      x \le y
                                     17 x
8)}
                    pc=3, x=s1, y=s2, s1>s2
                                                                    pc=8, x =s1, y=s2, s1\leqs2
                      x = x + y
                 pc=4, x =s1+s2, y=s2, s1>s2
                     y = x - y
                 pc=5, x =s1+s2, y=s1, s1>s2
                     x = x - y
                                                       x≤y
                  pc=6, x = s2, y = s1, s1 > s2
                                                                    pc=8, x =s2, y=s1, s1>s2
```

### **Another Example**

```
int f(int x) { return 2 * x ;}
int h(int x, int y) {
1) if (x!=y) {
                                             pc=1, x =s1, y=s2
 2) if (f(x) == x + 10) {
 3) abort() // * error */
                                                                *!=y
 4) return 0;
                 pc=4, x=s1, y=s2, s1=s2
                                                                pc=2, x = s1, y=s2, s1 \neq s2
                                            f(x) == x+10
                                                                   pc=4, x=s1, y=s2, s1\neqs2,
          pc=3, x =s1, y=s2, s1\neqs2,
                                                                          2*s1 \neq s2+10
                 2*s1 = s2+10
```

#### Non-Deterministic Behavior

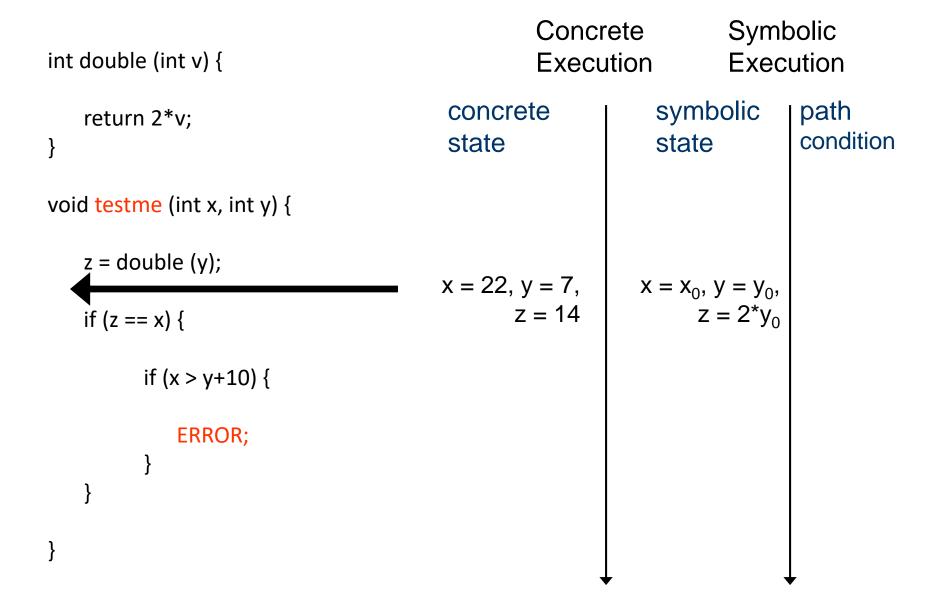
```
int x; y;
1) if (nondet()) {
    2) x = 7;
    }
else {
    3) x = 19;
}
```

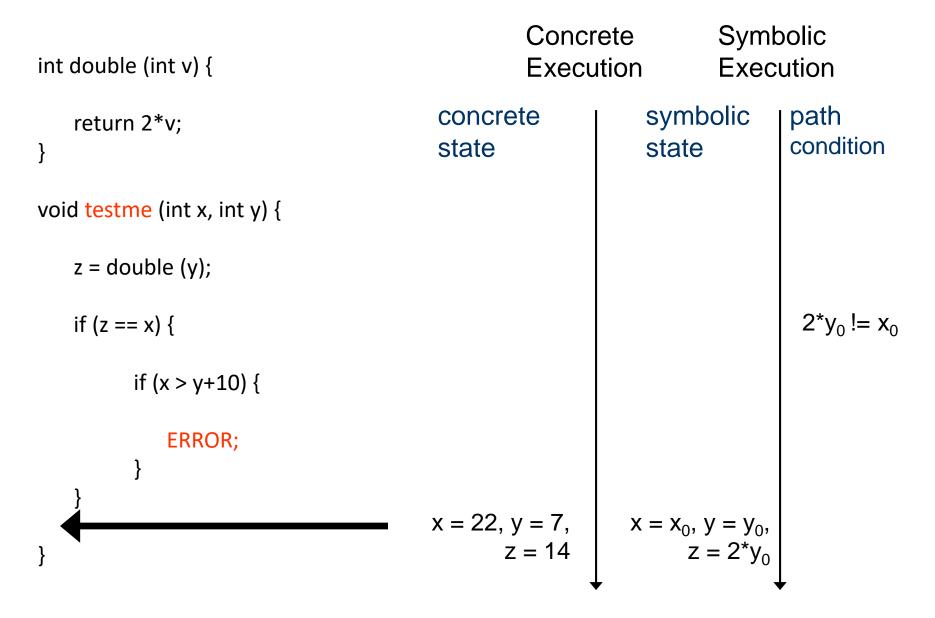
#### Loops

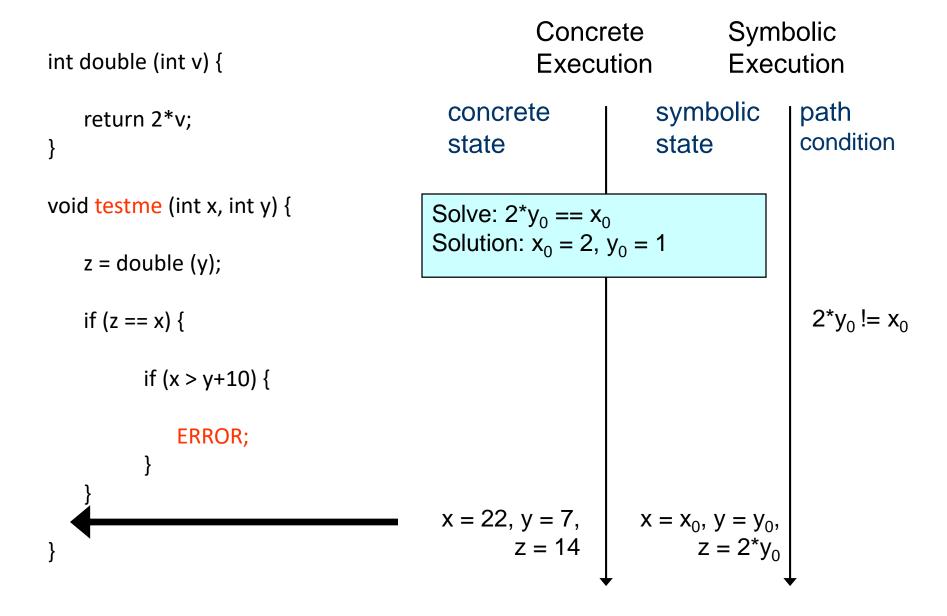
```
1) int i;
2) while i < n {
    i = i + 1;
    }
3) if (n ==10<sup>6</sup>) {
4) abort();
5) }
```

# Scaling Issues for Symbolic Exploration

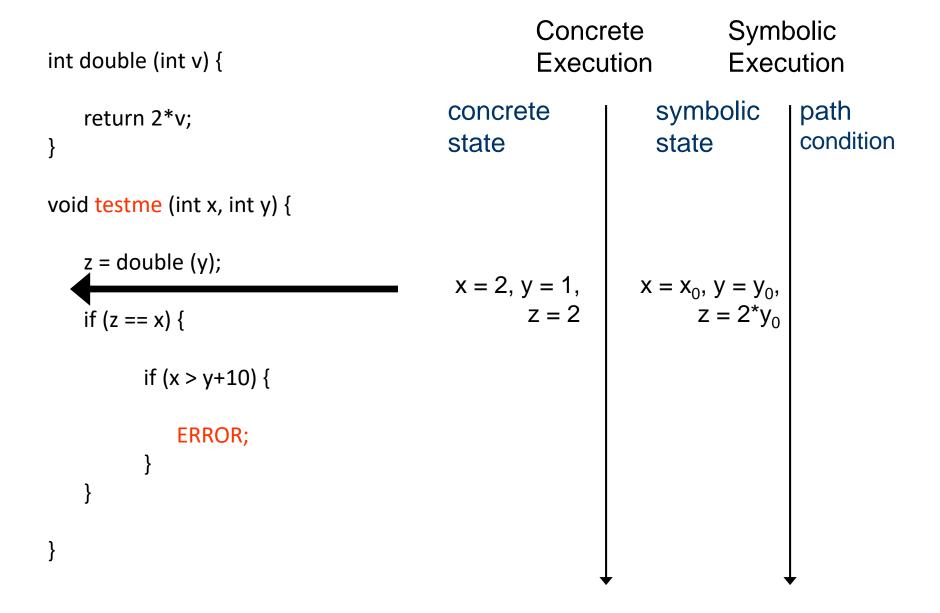
```
Symbolic
                                                Concrete
                                                Execution
                                                                    Execution
int double (int v) {
                                                            symbolic
                                        concrete
                                                                           path
   return 2*v;
                                                            state
                                                                           condition
                                        state
void testme (int x, int y) {
                                       x = 22, y = 7
                                                           x = x_0, y = y_0
   z = double(y);
   if (z == x) {
         if (x > y+10) {
            ERROR;
```

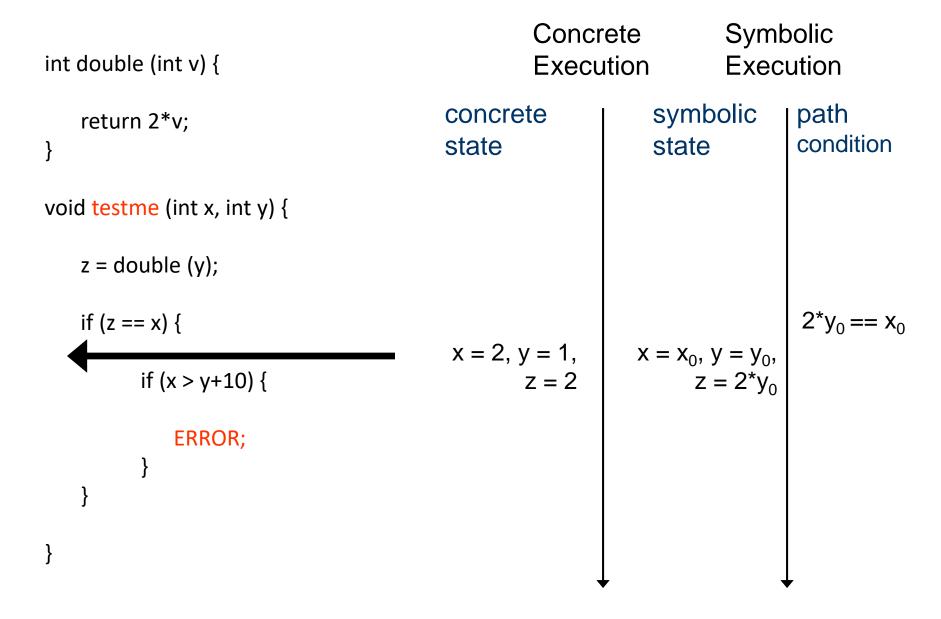


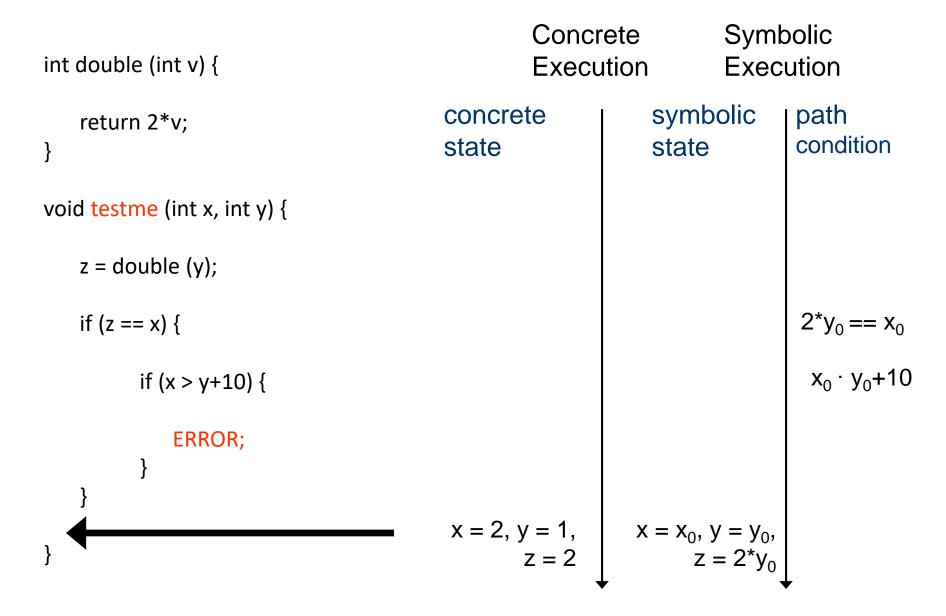


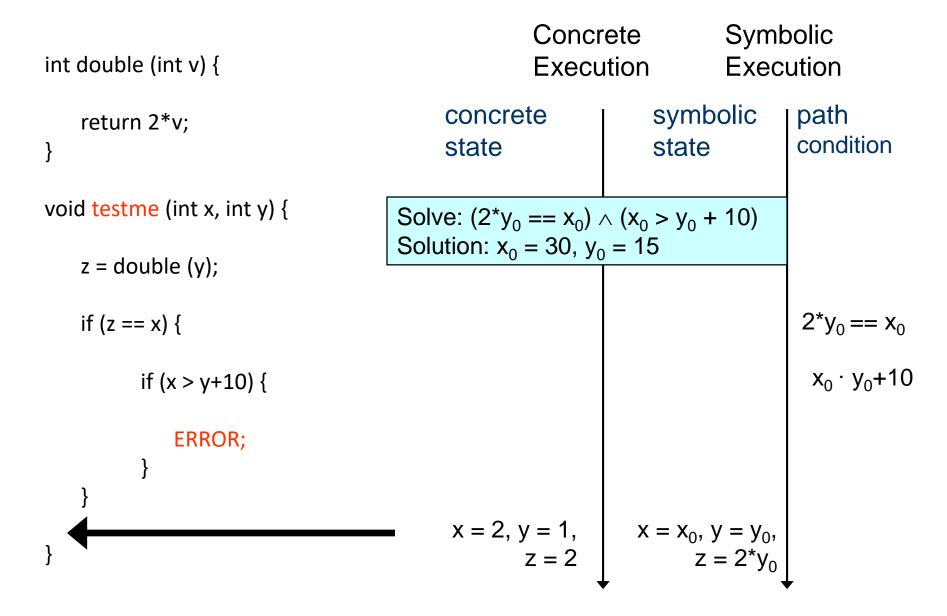


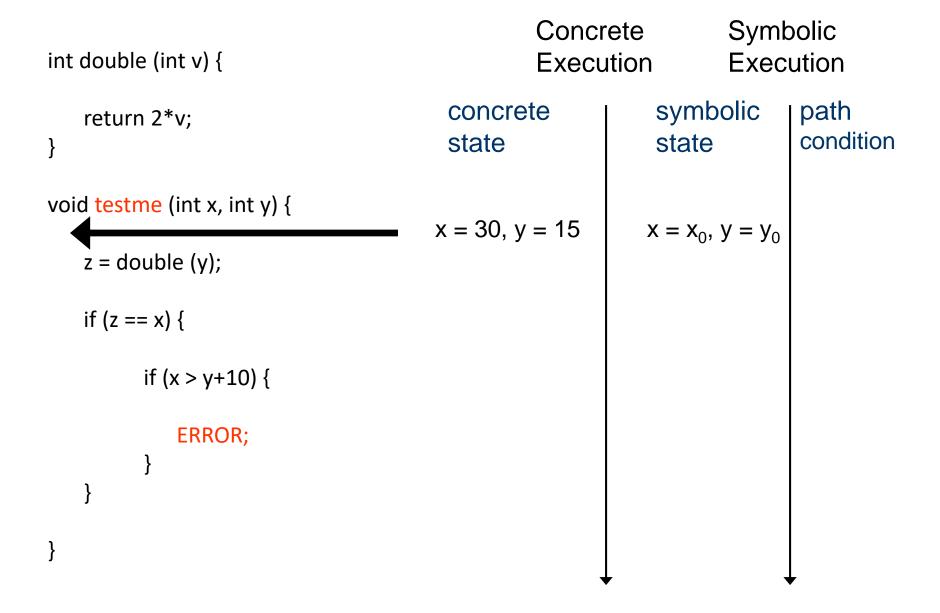
```
Symbolic
                                                 Concrete
                                                 Execution
                                                                    Execution
int double (int v) {
                                                             symbolic
                                        concrete
                                                                           path
   return 2*v;
                                                             state
                                                                           condition
                                        state
void testme (int x, int y) {
                                         x = 2, y = 1
                                                            x = x_0, y = y_0
   z = double(y);
   if (z == x) {
         if (x > y+10) {
            ERROR;
```











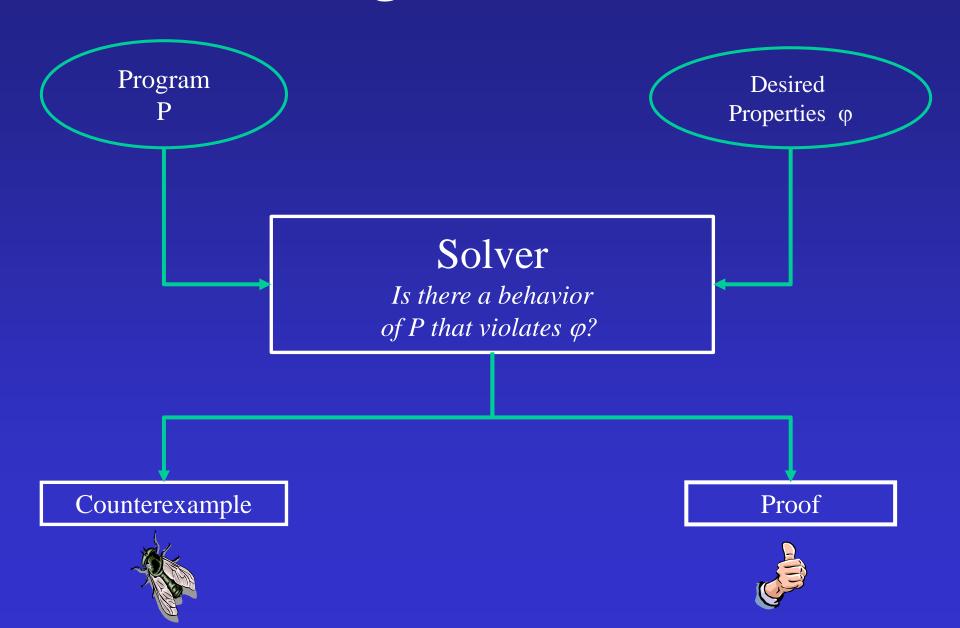
# The Concolic Testing Algorithm

Classify input variables into symbolic / concrete Instrument to record symbolic vars and path conditions Choose an arbitrary input Execute the program Symbolically re-execute the program Negate the unexplored last path condition Is there an input satisfying constraint

### SAGE: Whitebox Fuzzing for Security Testing

- Check correctness of Win'7, Win'8
- 200+ machine years
- 1 Billion+ SMT constraints
- 100s of apps, 100s of bugs
- 1/3 of all Win7 WEX security bugs found
- Millions of dollars saved

# **Automatic Program Verification**



### Example

```
int check_authentication(char *password) {
   int auth_flag = 0;
   char password_buffer[16];
   strcpy(password_buffer, password);
   if(strcmp(password_buffer, "brillig") == 0) auth_flag = 1;
   if(strcmp(password_buffer, "outgrabe") == 0) auth_flag = 1;
   return auth_flag;
int main(int argc, char *argv[]) {
  if(check_authentication(argv[1])) {
    printf("\n-=-=---\n");
    printf(" Access Granted.\n");
    printf("-=-=-=---\n");
 else
    printf("\nAccess Denied.\n");
```

# Undecidability

- The Halting Problem
  - Does the program P terminate on input I
- Rice's Theorem
  - Any non-trivial property of partial functions, there is no general and effective method to decide if program computes a partial function with that property

# Coping with Undecidability

- Permits occasional divergence
- Limited programs (not Turing Complete)
- Unsound Verification
  - Explore limited program executions
- Incomplete Verification
  - Explore superset of program executions
- Programmer Assistance
  - Inductive loop invariants

# Limited Programs

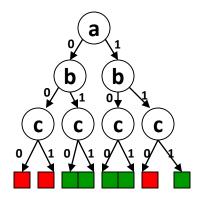
- Finite state programs
  - Finite state model checking
    - Explicit state SPIN, CHESS
    - Symbolic model checking SMV
- Loop free programs
  - Configuration files

# Unsound Verification

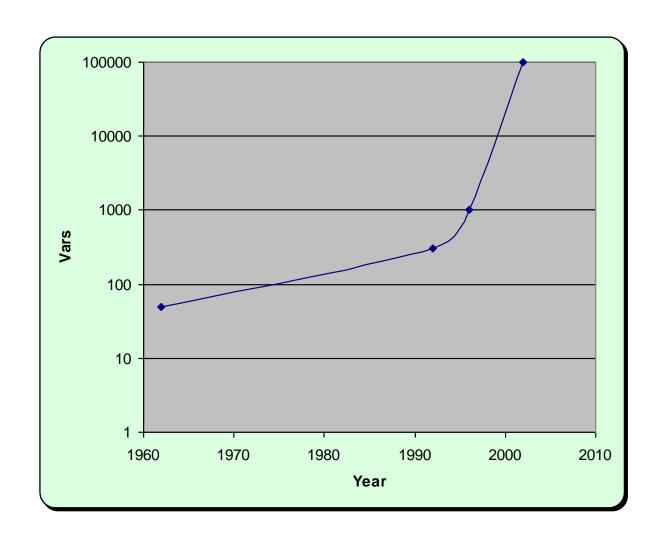
- Dynamic checking
  - Valgrind, Parasoft Insure, Purify, Eraser
- Bounded Model Checking
- Concolic Executions

## The SAT Problem

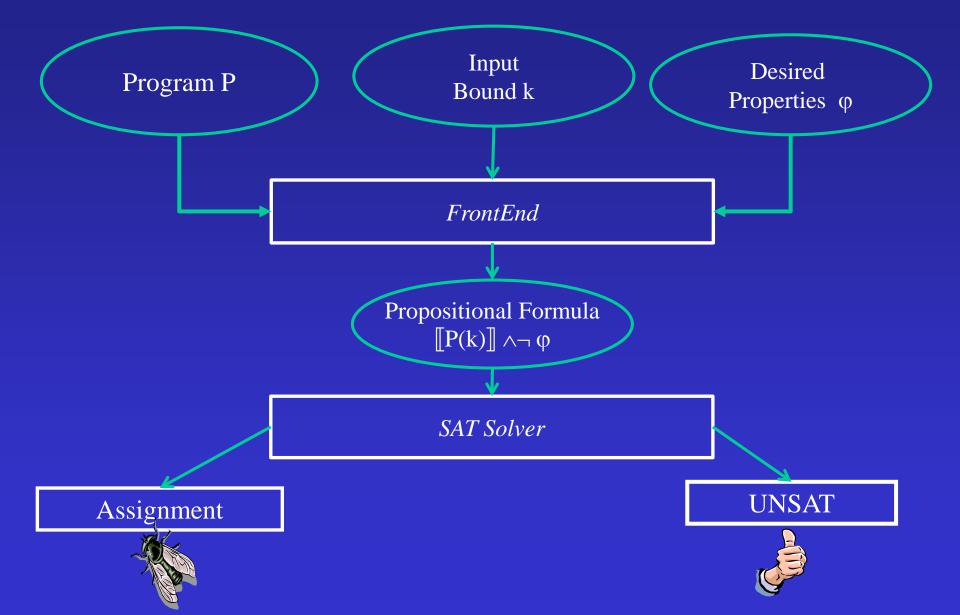
- Given a propositional formula (Boolean function)
  - $\varphi = (a \lor b) \land (\neg a \lor \neg b \lor c)$
- Determine if  $\phi$  is satisfiable
  - Find a satisfying assignment or report that such does not exit
- For *n* variables, there are 2<sup>n</sup> possible truth assignments to be checked



# SAT made some progress...



# **Bounded Model Checking**



# A Simple Example

#### Program

```
int x;
int y=8, z=0, w=0;
if (x)
  z = y - 1;
else
  w = y + 1;
assert (z == 5 ||
  w == 9)
```

#### Constraints

SAT counterexample found!

$$y = 8,$$
  
 $z = x ? y - 1 : 0,$   
 $w = x ? 0 : y + 1,$   
 $z != 5,$   
 $w != 9$ 

$$y = 8, x = 1, w = 0, z = 7$$

# A Simple Example

#### Program

```
int x;
int y=8,z=0,w=0;
if (x)
  z = y - 1;
else
  w = y + 1;
assert (z == 7 ||
  w == 9)
```

#### Constraints

$$y = 8,$$
  
 $z = x ? y - 1 : 0,$   
 $w = x ? 0 : y + 1,$   
 $z != 7,$   
 $w != 9$ 

UNSAT
Assertion always holds!

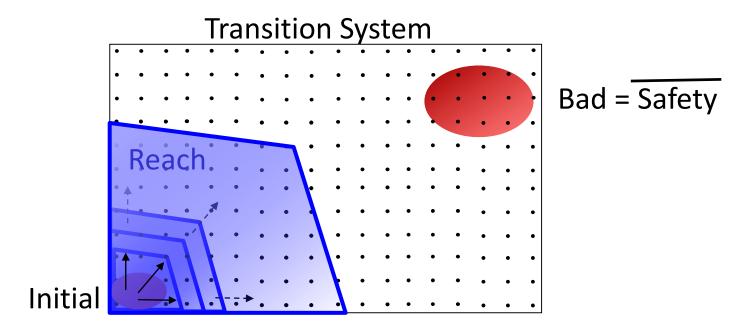
# Summary Bounded Model Checking

- Excellent tools exist (CBMC, Alloy)
- Many bugs occur on small inputs
- Useful for designs too
- Scalability is an issue
- Challenging features
  - Bounded arithmetic
  - Pointers and Heap
  - Procedures
  - Concurrency

# Success Stories BMC

- Car industry
- Amazon
- Regression

# Safety of Transition Systems



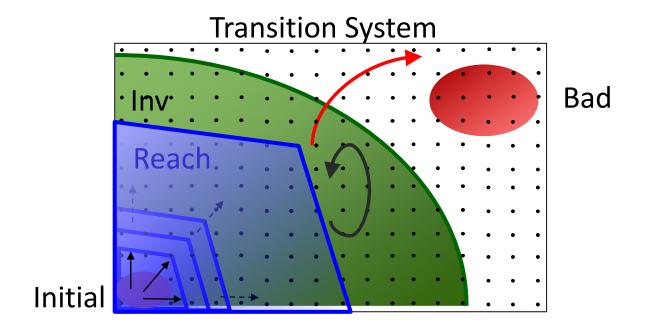
System S is **safe** if no bad state is reachable

 $R_0 = Init$  – Initial states, reachable in 0 transitions  $R_{i+1} = R_i \ U\{\sigma' \mid \sigma \rightarrow \sigma' \ and \ \sigma \in R_i\}$   $R = R_0 \ UR_1 \ UR_2 \ U...$ 

Safety:  $R \cap Bad = \emptyset$ 

K-Safety:  $R_K \cap Bad = \emptyset$ 

## Inductive Invariants



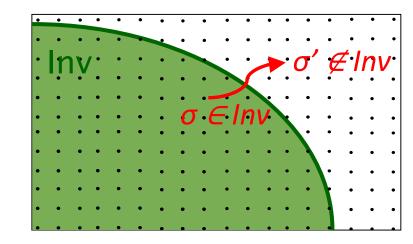
System S is safe if no bad state is reachable System S is safe iff there exists an inductive invariant Inv s.t.:

```
Inv \cap Bad = \emptyset (Safety)
Init \subseteq Inv (Initiation)
if \sigma \in Inv and \sigma \xrightarrow{} \sigma' then \sigma' \in Inv (Consecution)
```

# Counterexample To Induction (CTI)

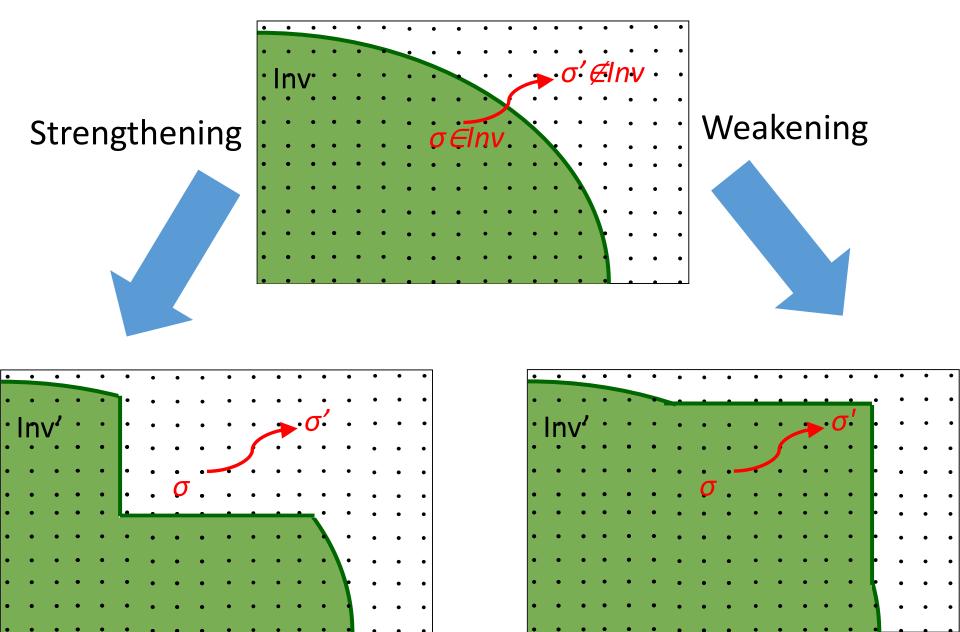
#### States $\sigma, \sigma'$ are a CTI of Inv if:

- σ ∈ Inv
- σ' ∉ Inv
- $\sigma \rightarrow \sigma'$

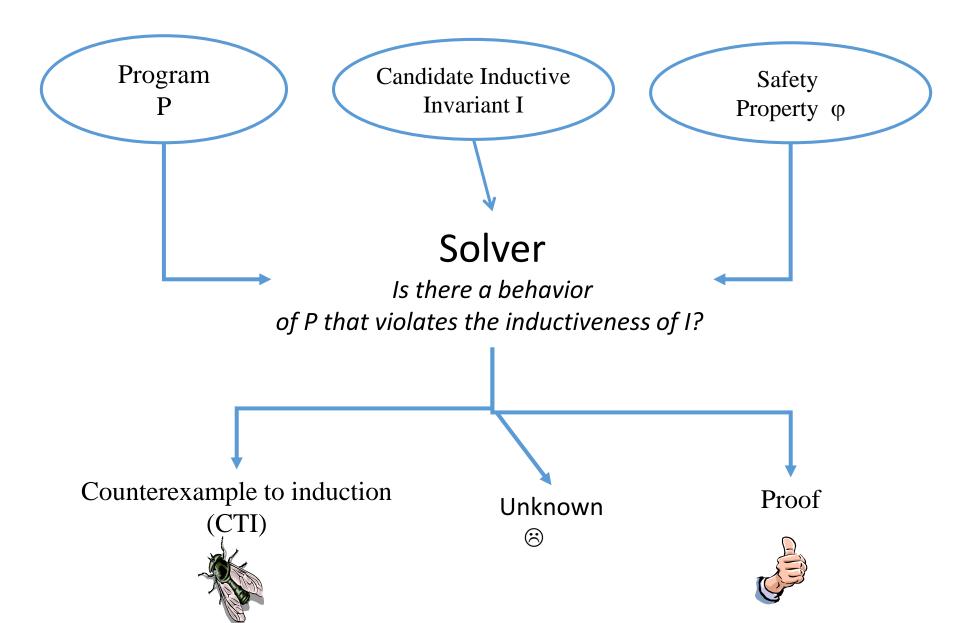


- A CTI may indicate:
  - A bug in the system
  - A bug in the safety property
  - A bug in the invariant
    - Too weak
    - Too strong

# Strengthening & Weakening from CTI



# Deductive (Semi-Automatic) Verification



### **Deductive Verification**

```
1: x := 1;

2: y := 2;

while * do {

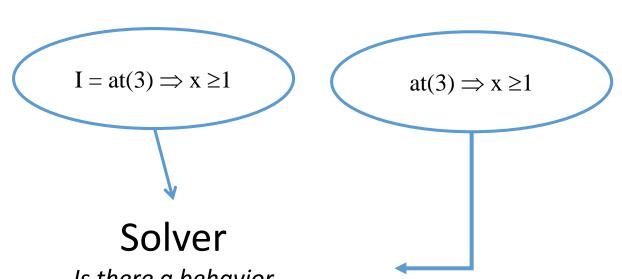
3: assert x ≥1;

4: x:= x + y;

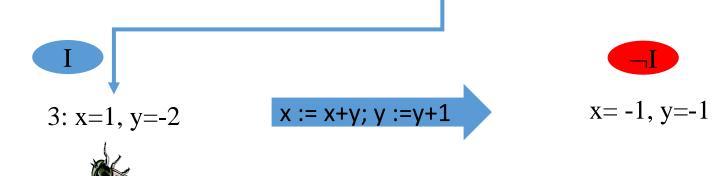
5: y := y + 1

}

6:
```



Is there a behavior of P that violates the inductiveness of I?



### **Deductive Verification**

```
1: x := 1;

2: y := 2;

while * do {

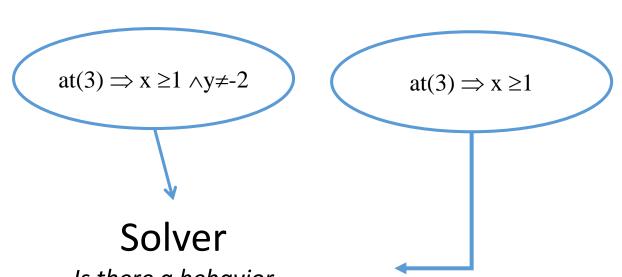
3: assert x ≥1;

4: x:= x + y;

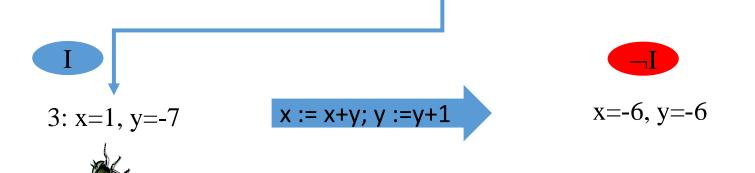
5: y := y + 1

}

6:
```



Is there a behavior of P that violates the inductiveness of I?



### **Deductive Verification**

```
1: x := 1;

2: y := 2;

while * do {

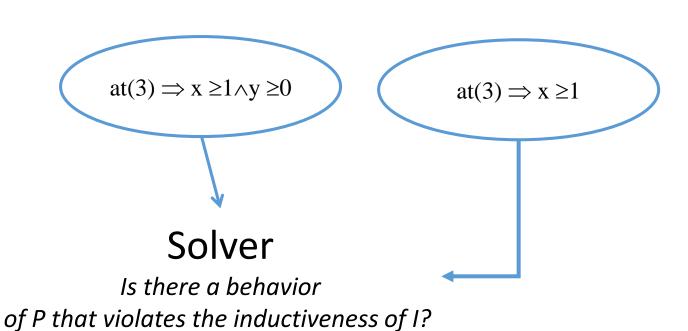
3: assert x ≥1;

4: x:= x + y;

5: y := y + 1

}

6:
```







# Algorithmic Deductive Verification

- SAT/SMT has made huge progress in the last decade
- Great impact on verification:
   Dafny[ITP'13], IronClad/IronFleet[SOSP'15], and more
- State: finite first-order structure over vocabulary V
- Initial states and safety property (first-order formulas):
  - Init(V) initial states
  - Bad(V) bad states
- Transition relation:

first-order formula TR(V, V')
V' is a copy of V describing the next state

[ITP'13] K.R. Leino: Automating Theorem Proving with SMT. DAFNY [SOSP'15] C. Hawblitzel, J. Howell, M. Kapritsos, J.R. Lorch, B. Parno, M. Roberts, S. Setty, B. Zill: IronFleet: proving practical distributed systems correct

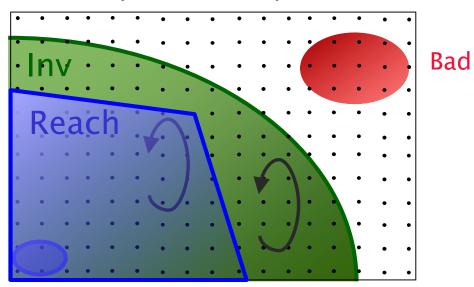
# Algorithmically Checking Inductiveness

#### Inv is an **inductive invariant** if:

Initiation: Init ⇒ Inv Init ¬Inv unsat
 Safety: Inv ⇒ ¬Bad Inv Bad unsat

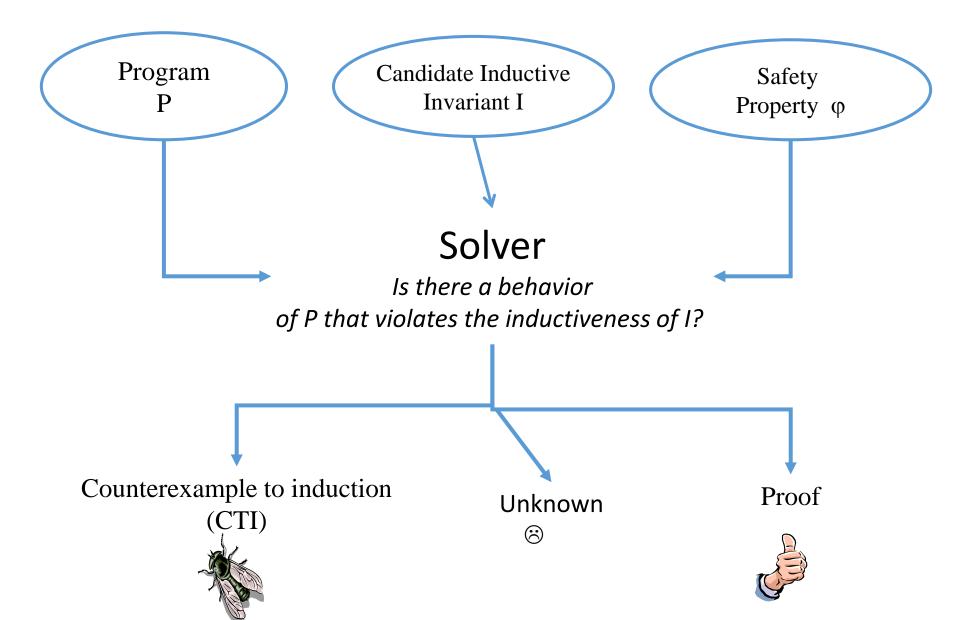
• Consecution:  $\operatorname{Inv} \wedge \operatorname{TR} \Rightarrow \operatorname{Inv}' \quad \operatorname{Inv} \wedge \operatorname{TR} \wedge \neg \operatorname{Inv}' \text{ unsat}$ 

#### System State Space



Initial

#### Algorithmic Deductive Verification



#### Challenges

- 1. Formal specification:
  - Modeling the system (TR, Init)
  - Formalizing the safety property (Bad)
- 2. Inductive Invariants (Inv)
  - Hard to specify manually
  - Hard to maintain
  - Hard to infer automatically
- 3. Deduction Checking inductiveness
  - Undecidability of implication checking
    - Unbounded state, arithmetic, quantifier alternation

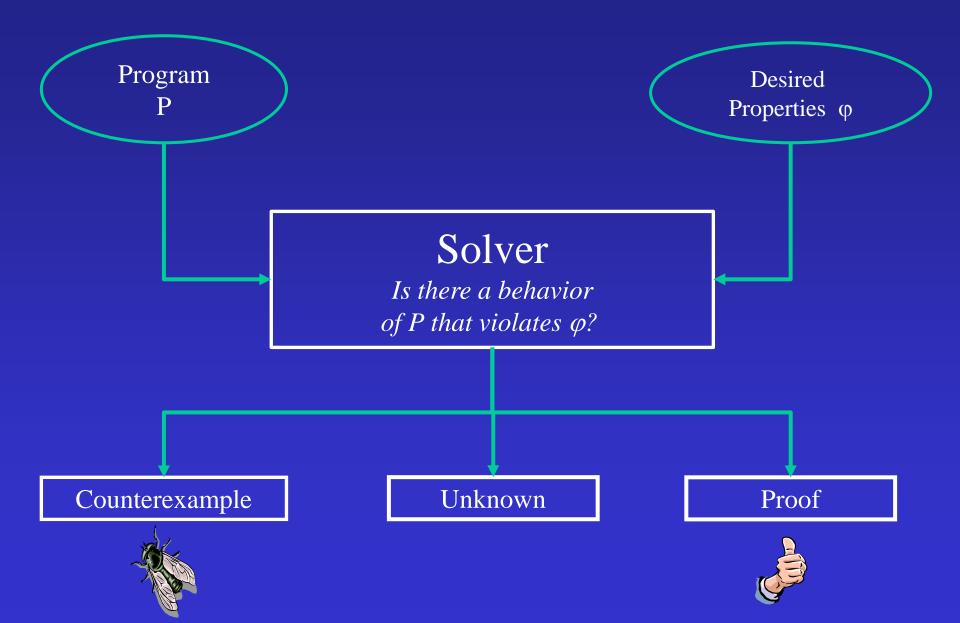
# Existing Approaches for Verification

- Automated invariant inference
  - Abstract Interpretation
    - Ultimately limited due to undecidability
- Use SMT for deduction with manual program annotations (e.g. Dafny)
  - Requires programmer effort to provide inductive invariants
  - SMT solver may diverge (matching loops, arithmetic)
- Interactive theorem provers (e.g. Coq, Isabelle/HOL)
  - Programmer gives inductive invariant and proves it
  - Huge effort (10-50 lines of proof per line of code)

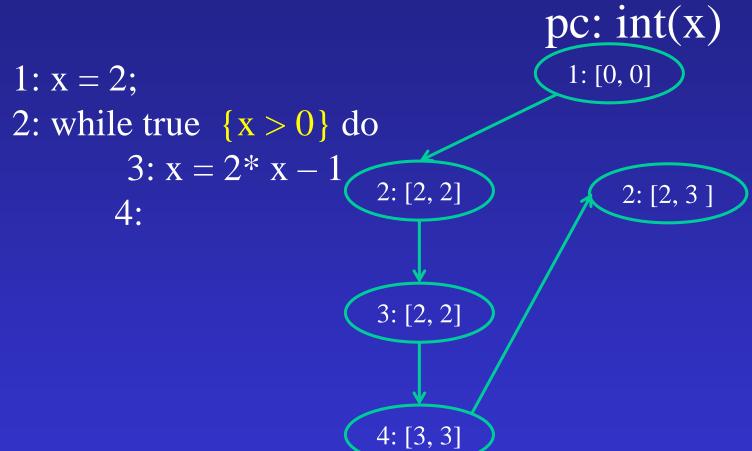
#### Abstract Interpretation

- Automatically prove that the program is correct by also considering infeasible executions
- Abstract interpretation of program statements/conditions
- Conceptually explore a superset of reachable states
- Sound but incomplete reasoning
- Automatically infer sound inductive invariants

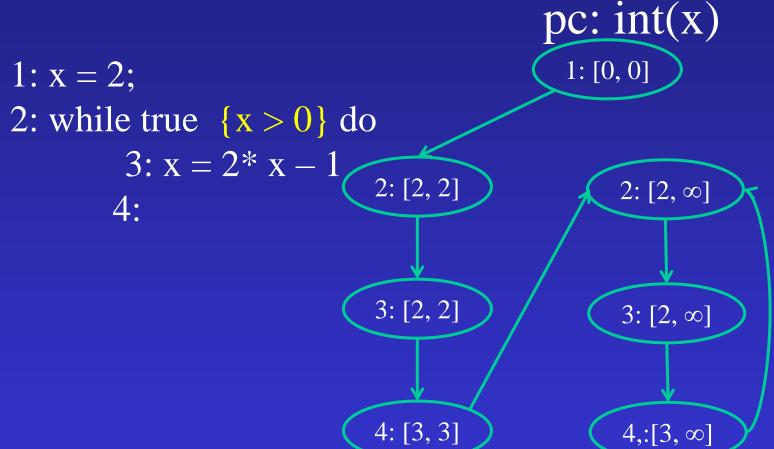
#### **Automatic Program Verification**



# Interval Based Abstract Interpretation



# Interval Based Abstract Interpretation



# Interval Based Abstract Interpretation pc: int(x), int(y)

1: 
$$x = 2$$
,  $y = 2$   
2: while true  $\{x = y\}$  do  
3:  $x = 2*x - 1$   
 $y = 2*y - 1$   
4:  $x = 2$ ,  $y = 2$   
2:  $[2, 2]$ ,  $[2, 2]$   
3:  $[2, 2]$ ,  $[2, 2]$ 

4: [3, 3], [3, 3]

#### Shape-Based Abstract Interpretation

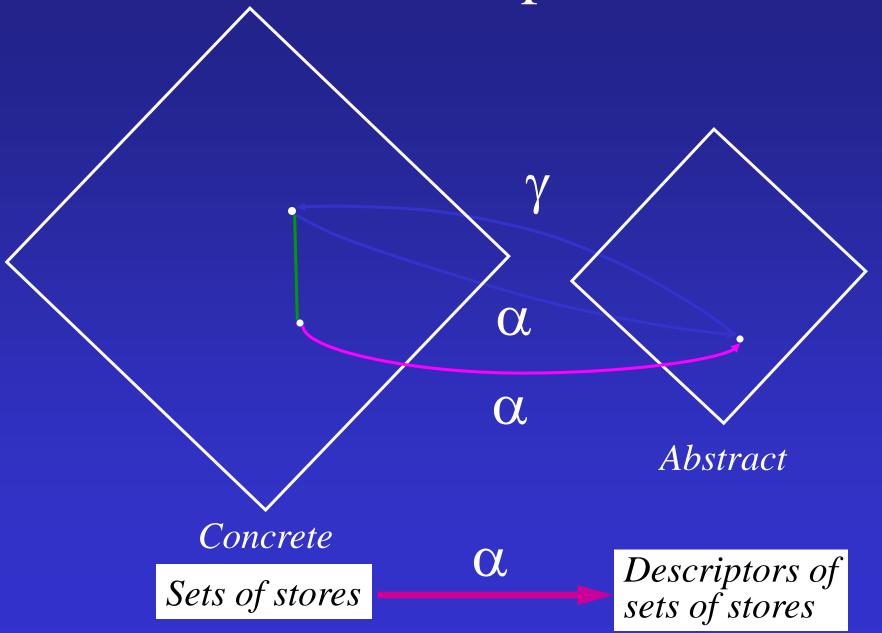
```
node search(node h, int v) {
  1: node x = h;
  2: while (h != NULL) {
    3: if (x->d == v) return x;
    4: assert x != null; x = x->n;
 5: return (node) NULL
```

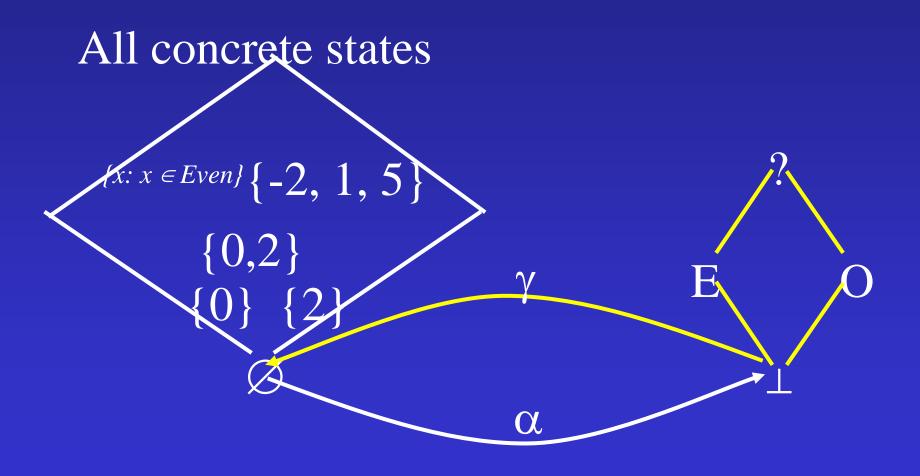
#### Shape-Based Abstract Interpretation

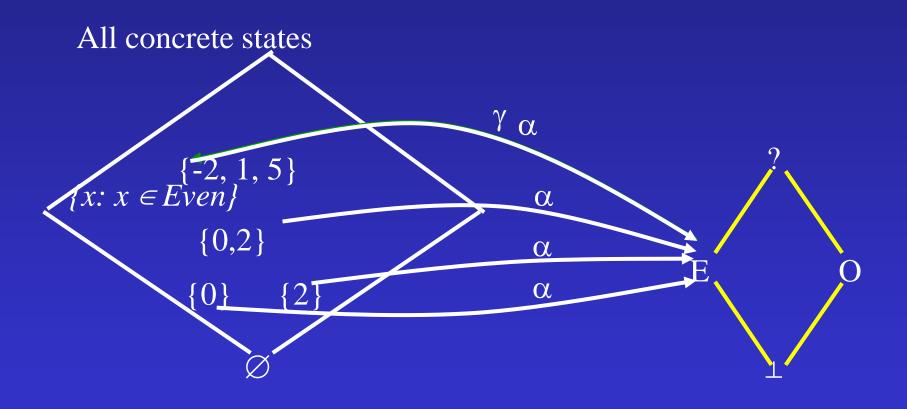
```
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  1: node x = h;
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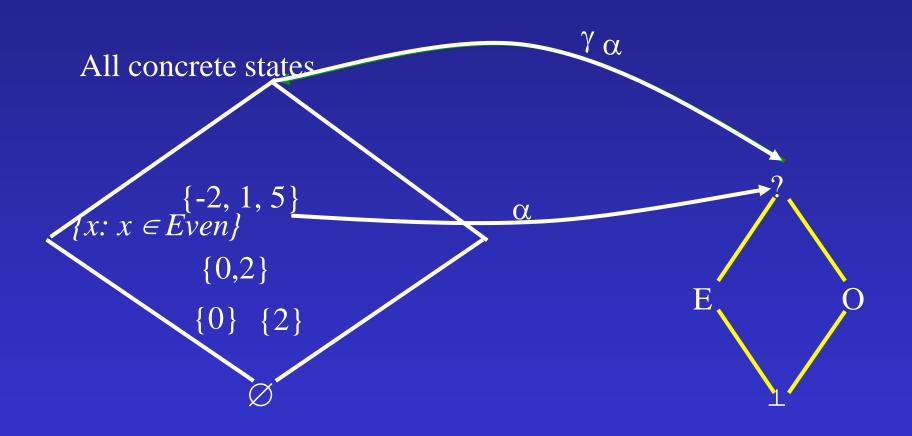
```
1: while (x !=1) do {
        2: if (x \% 2) == 0
                                                                 6: O
            \{ 3: x := x / 2; \}
            else
           \{ 4 : x := x * 3 + 1;
                                                    2: ?
          5: assert (x \%2 ==0); }
6: }
                                            3: E
                                                            4: O
                                                            5: E
```

## Abstract Interpretation

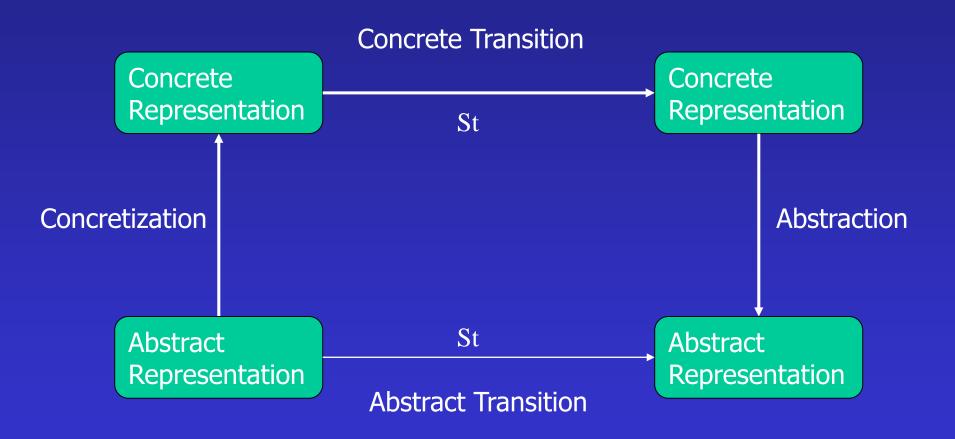








#### (Best) Abstract Transformer



```
1: while (x !=1) do {
        2: if (x \% 2) == 0
                                                                6: O
            \{ 3: x := x / 2; \}
            else
           {4: x := x * 3 + 1;}
                                                   2: ?
          5: assert (x \%2 ==0); }
6: }
                                           3: E
                                                           4: O
                                                           5: E
```

#### Summary Abstract Interpretation

- Conceptual method for building static analyzers
- A lot of techniques:
  - join, meet, widening, narrowing, procedures
- Can be combined with theorem provers



#### Rules



Read for understanding

New API rules

#### Static Driver Verifier

Precise API Usage Rules (SLIC) Drive testing tools



Development Defects



100% path coverage

Software Model Checking Testing

"Things like even Itware verification, this has been the Holy Grail of computer science for many decales but now in some very key areas, for example, driver is Source Code and how it works in order to guarantee the reliability" Bill Gates

#### Success Story: Astrée

- Developed at ENS
- A tool for checking the absence of runtime errors in Airbus flight software





[CC'00] R. Shaham, E.K. Kolodner, S. Sagiv:

Automatic Removal of Array Memory Leaks in Java

[WCRE'2001] A. Miné: The Octagon Abstract Domain

[PLDI'03] B. Blanchet, P. Cousot, R. Cousot, J. Feret, L. Mauborgne, A. Miné,

D. Monniaux, X. Rival: A static analyzer for large safety-critical software

# PANSACCESS: Panaya Making ERP easy

- Static analysis to detect the impact of a change for ERP professionals (slicing)
- Developed by N. Dor and Y. Cohen
- Acquired by Infosys

[ISSTA'08] N. Dor, T. Lev-Ami, S. Litvak, M. Sagiv, D. Weiss:

Customization change impact analysis for erp professionals via program slicing

[FSE'10] S. Litvak, N. Dor, R. Bodík, N. Rinetzky, M. Sagiv: Field-sensitive program dependence analysi

# Exciting Times for Formal Methods

- Adapted by the Network and System's communities
- The beginning of industry adaption
- New applications
  - Networks
  - Biology
  - Education

## Tentative Schedule

Week	Lecture	Recitation	Exercise
1	Overview	No Recitation	No assignment
2	SAT and SMT Solvers	Z3	Graph algorithms with Z3
3	Bounded Model Checking	СВМС	СВМС
4	Concolic Testing	KLEE	KLEE
5	Deductive Verification 1	No Recitation	No assignment
6	Deductive Verification 2	Dafny	Dafny
7	Static Analysis	Apron, Absint	Apron and AbsInt
8	Random Testing	Quickcheck, Randoop, Simullant, Autotest, YETI, GramTest	Use the tools
9	Fuzz Testing	TBD	TBD
10	Mutation Testing	TBD	TBD
11	Unit Testing	TBD	TBD
12	Delta Debugging	TBD	TBD
13	Program Synthesis	TBD	TBD
14	System's Code	TBD	TBD
15	Network and Cloud	TBD	TBD

#### Course Benefits

- Learn about research which is becoming mature
- Understand the limits of formal methods